Electronic Acknowledgement Receipt FES ID: 1137547 Application Number: 10777261 Confirmation Number: 5407 Apparatus and method for performing static timing analysis of an Title of Invention: integrated circuit design using dummy edge modeling First Named Inventor: Craig Marshall Darsow Customer Number: 46296 Filer: Derek P. Martin Filer Authorized By: Attorney Docket Number: ROC920030338US1 Receipt Date: 02-AUG-2006 Filing Date: 12-FEB-2004 Time Stamp: 12:57:40 Application Type: Utility International Application Number:

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1	Response to Election / Restriction Filed	election_and_amendment.pd f	81101	no	9

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National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt. in due course.